

FACULTY OF ENGINEERING

DEPARTMENT OF ELECTRONIC AND ELECTRICAL ENGINEERING

ELECTRONIC AND ELECTRICAL ENGINEERING (January Start)

Master of Science in Electronic and Electrical Engineering
Postgraduate Diploma in Electronic and Electrical Engineering
Postgraduate Certificate in Electronic and Electrical Engineering

These regulations are to be read in conjunction with [General Academic Regulations - Postgraduate Taught Degree Programme Level](#).

Admission

1. Notwithstanding the [General Academic Regulations - Postgraduate Taught Degree Programme Level](#), applicants shall possess:
 - i. a first or second class Honours degree (in Electrical or Electronic Engineering in a cognate subject) from a United Kingdom university; or
 - ii. a qualification deemed by the Programme Director acting on behalf of Senate to be equivalent; or
 - iii. appropriate professional experience.
2. In all cases, applicants whose first language is not English, shall be required to demonstrate an appropriate level of English.

Duration of Study

3. The [General Academic Regulations - Postgraduate Taught Degree Programme Level](#) shall apply.

Mode of Study

4. The programme is available by full-time study only, starting every January.

Curriculum

5. All students shall undertake an approved curriculum as follows:
 - i. for the Postgraduate Certificate no fewer than 60 credits from the lists of taught modules.
 - ii. for the Postgraduate Diploma no fewer than 120 credits including all the compulsory modules.
 - iii. for the degree of MSc no fewer than 180 credits including the EE990 project.

Compulsory Modules

Module Code	Module Title	Level	Credits
EE886	Assignment and Professional Studies	5	20
Students for the degree of MSc only:			
EE990	MSc Project	5	60

Optional Modules

All students shall select 100 credits with at least 30 credits from both List A and List B:

List A:

Module Code	Module Title	Level	Credits
EE769	Digital Signal Processing Principles	5	20
EE820	Offshore Wind Farm O&M and Economics	5	10
EE826	Software Defined Radio	5	10
EE874	High Voltage Technology Principles	5	10
EE875	Power Electronics Principles	5	10
EE885	Software Design & Programming for Engineering	5	10
CS952	Database & Web System Development	5	20
CS985	Machine Learning for Data Analytics	5	20

List B:

Module Code	Module Title	Level	Credits
EE821	Wind Generators Modelling & Control	5	10
EE866	Power Electronic Devices, Drives & Machines 1	5	10
EE669	Digital Signal Processing Principles	5	10
EE867	Power System Analysis and Protection	5	10
EE825	Data Transmission & Physical Layer Radio Networks	5	10
EE872	Control Principles 1	5	10
EE876	Power System Economics & Markets	5	10
EE877	Wind Energy & Distributed Energy Resources 1	5	10
EE878	Advanced DSP 1	5	10
EE880	Embedded System Design 1	5	10
EE681	Image Processing	5	10
EE887	Industrial Sensor Technologies	5	10

Exceptionally, such other modules totalling no more than 20 credits, as approved by the Programme Director.

Students may not select any modules from the list of optional modules which they have previously successfully completed. Not all modules in these lists will be available every year.

Examination, Progress and Final Assessment

6. The [General Academic Regulations - Postgraduate Taught Degree Programme Level](#) shall apply.

Award

7. **Degree of MSc:** In order to qualify for the award of the degree of MSc in Electronic and Electrical Engineering, a candidate must have performed to the satisfaction of the Board of Examiners and must have accumulated no fewer than 180 credits, of which 60 must have been awarded in respect of the Project EE990.
8. **Postgraduate Diploma:** In order to qualify for the award of the Postgraduate Diploma in Electronic and Electrical Engineering, a candidate must have accumulated no fewer than 120 credits from the taught programme curriculum.
9. **Postgraduate Certificate:** In order to qualify for the award of the Postgraduate Certificate in Electronic and Electrical Engineering, a candidate must have accumulated no fewer than 60 credits from the taught programme curriculum.